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S/N 10/050621

**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

CHENG et al.

Examiner:

Minh N. Trinh

Serial No.:

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Group Art Unit:

3729

Filed:

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Docket No.:

08688.0258US01

Title:

METHOD OF FABRICATING A MULTI-LAYER CIRCUIT BOARD

**ASSEMBLY** 

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademork Office on November 12,

2004.

Name: Shoundra Pissmann

## RESPONSE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed July 16, 2004, the following comments are provided:

Listing of Claims begins on page 2 of this paper.

Remarks begin on page 4 of this paper.